

What is claimed is:

1. A microprocessor combined with and used in a pair with a baseband processor for performing baseband processing which enables exchange of information by using radio waves, comprising

a central processing unit for calculation processing, a counter for measuring time in the calculation processing by the central processing unit, and an interface which enables the baseband processor to read the counter.

2. The microprocessor according to Claim 1, further comprising

a digital signal processor capable of compressing and decompressing image data transferred from a camera module, wherein

the central processing unit instructs the digital signal processor to start the compression or the decompression of image data for one frame, according to an interruption signal from the camera module, every time of finishing each transfer of the image data for one frame from the camera module, and resets the counter.

3. The microprocessor according to Claim 1, further comprising

a memory for storing voice data compressed by the baseband processor and taken in through the interface and video data compressed by the digital signal processor, wherein

each of the voice data and the video data has a data structure in which time information with a frame boundary of the video used as a reference is attached as a header for every unit of a predetermined block.

4. The microprocessor according to Claim 1, further comprising

a memory for storing voice data compressed by the baseband processor and taken in through the interface and video data compressed by the digital signal processor, wherein

the voice data and the video data have a data structure in which the voice data and the video data are collectively handled by the unit of data synchronous with each other.

5. The microprocessor according to Claim 2, further comprising

a command register capable of setting a command through the baseband processor, an address register capable of setting an address signal through the baseband processor, a data register capable of reading and writing data from the baseband processor, a command decoder for decoding the command set in the command register, and an address decoder for decoding the address signal of the address register, wherein

the counter is selected according to the decode result of the address decoder and a path capable of transferring stored information of the counter to the data register is selectively formed according to the decode result of the command decoder.

6. A microprocessor combined with and used in a pair with a baseband processor for performing baseband processing which enables exchange of information by using radio waves, comprising

a central processing unit for calculation processing, a counter for measuring time in the calculation processing by the central processing unit, a register of interruption parameter information readable from the baseband processor, and a digital signal processor capable of compressing and decompressing image data transferred from a camera module, wherein

the central processing unit instructs the digital signal processor to start the compression or the decompression of image data for one frame, according to an interruption signal from the camera module, every time of finishing each transfer of the image data for one frame from the camera module, resets the counter, sets an information bit indicating that a parameter of the interruption is reset of the counter, in the register of interruption parameter information, and transmits an interruption signal to the baseband processor.

7. A mobile communication terminal including a baseband processor for performing baseband processing which enables exchange of information by using radio waves and an application processor combined with and used in a pair with the baseband processor, wherein

the application processor comprises a central processing unit for calculation processing, a counter for measuring time in the calculation processing by the central processing unit, and an interface which enables the baseband processor to read the counter.

8. The mobile communication terminal according to Claim 7, further comprising

a camera module for obtaining video data and

a digital signal processor capable of compressing and decompressing the video data transferred from the camera module, wherein

the central processing unit instructs the digital signal processor to start the compression or the decompression of the video data for one frame, according to an interruption signal from the camera module, every time of finishing each transfer of the video data for one frame from the camera module and resets the counter.

9. The mobile communication terminal according to Claim 8, wherein

the application processor comprises a memory for storing voice data compressed by the baseband processor and taken in through the interface and the video data compressed by the digital signal processor, and each of the voice data and the video data has a data structure in which time information with a frame boundary of the video used as a reference is attached

as a header for every unit of a predetermined block.

10. The mobile communication terminal according to Claim 8, wherein

the application processor comprises a memory for storing voice data compressed by the baseband processor and taken in through the interface and the video data compressed by the digital signal processor and the voice data and the video data have a data structure in which the voice data and the video data are collectively handled by the unit of data synchronous with each other.

11. The mobile communication terminal according to Claim 10, wherein

the application processor further comprises a command register capable of setting a command through the baseband processor, an address register capable of setting an address signal through the baseband processor, a data register capable of reading and writing data through the baseband processor, a command decoder for decoding the command set in the command register, and an address decoder for decoding the address signal of the address register, the counter is selected according to the decode result of the address decoder, and a path capable of transferring stored information of the counter to the data register is selectively formed according to the decode result of the command decoder.

12. A mobile communication terminal including a baseband

processor for performing baseband processing which enables exchange of information by using radio waves and an application processor combined with and used in a pair with the baseband processor, in which

the application processor comprises a central processing unit for calculation processing, a counter for measuring time in the calculation processing by the central processing unit, a register of interruption parameter information readable from the baseband processor, and a digital signal processor capable of compressing and decompressing image data transferred from a camera module, and

the central processing unit instructs the digital signal processor to start the compression or the decompression of the image data for one frame, according to an interruption signal from the camera module, every time of finishing each transfer of the image data for one frame from the camera module, resets the counter, sets an information bit indicating that a parameter of the interruption is reset of the counter in the register of interruption parameter information, and transmits an interruption signal to the baseband processor.